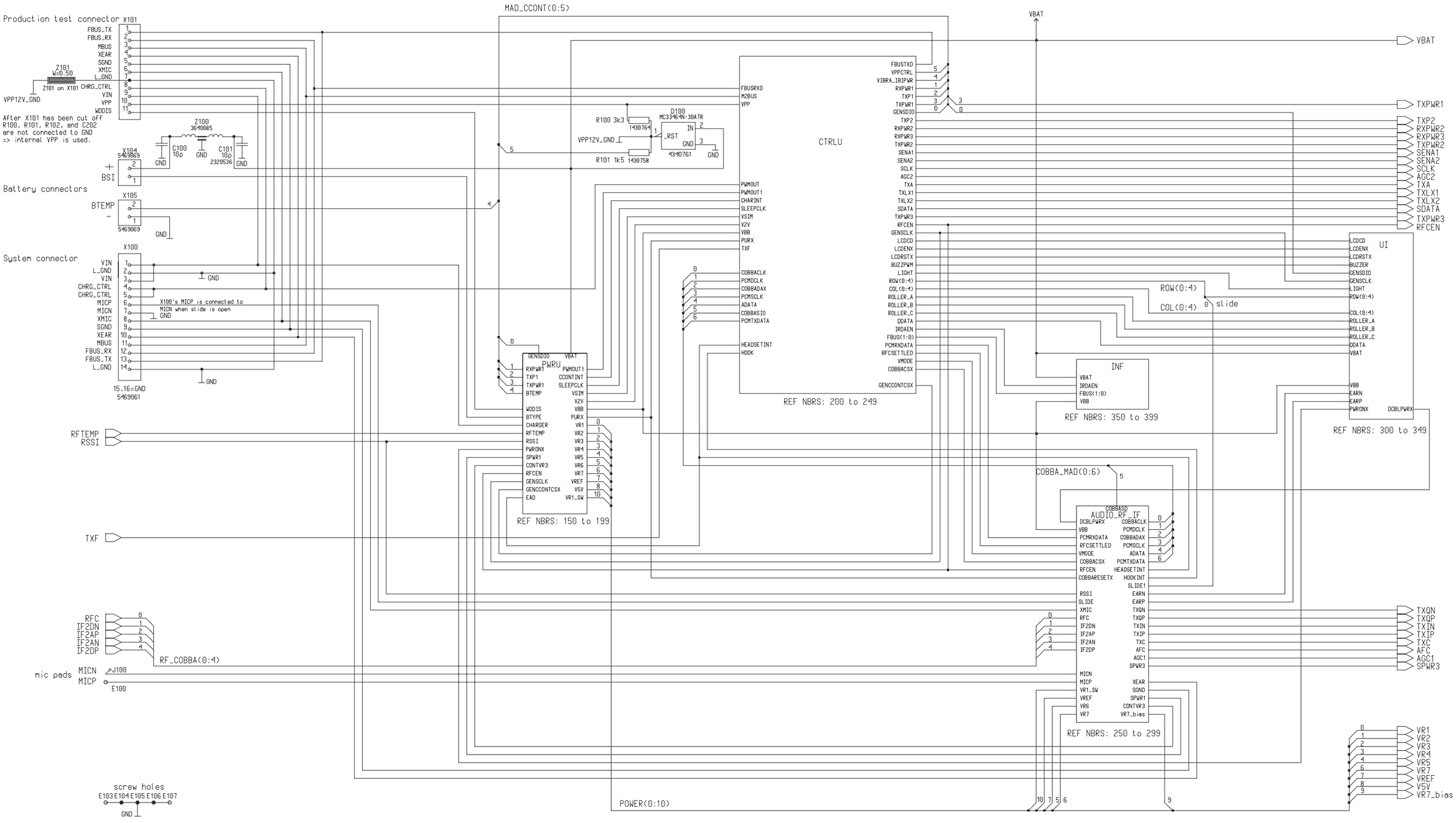
Block Diagram of UT5 (Version 17.4 Edit 40)



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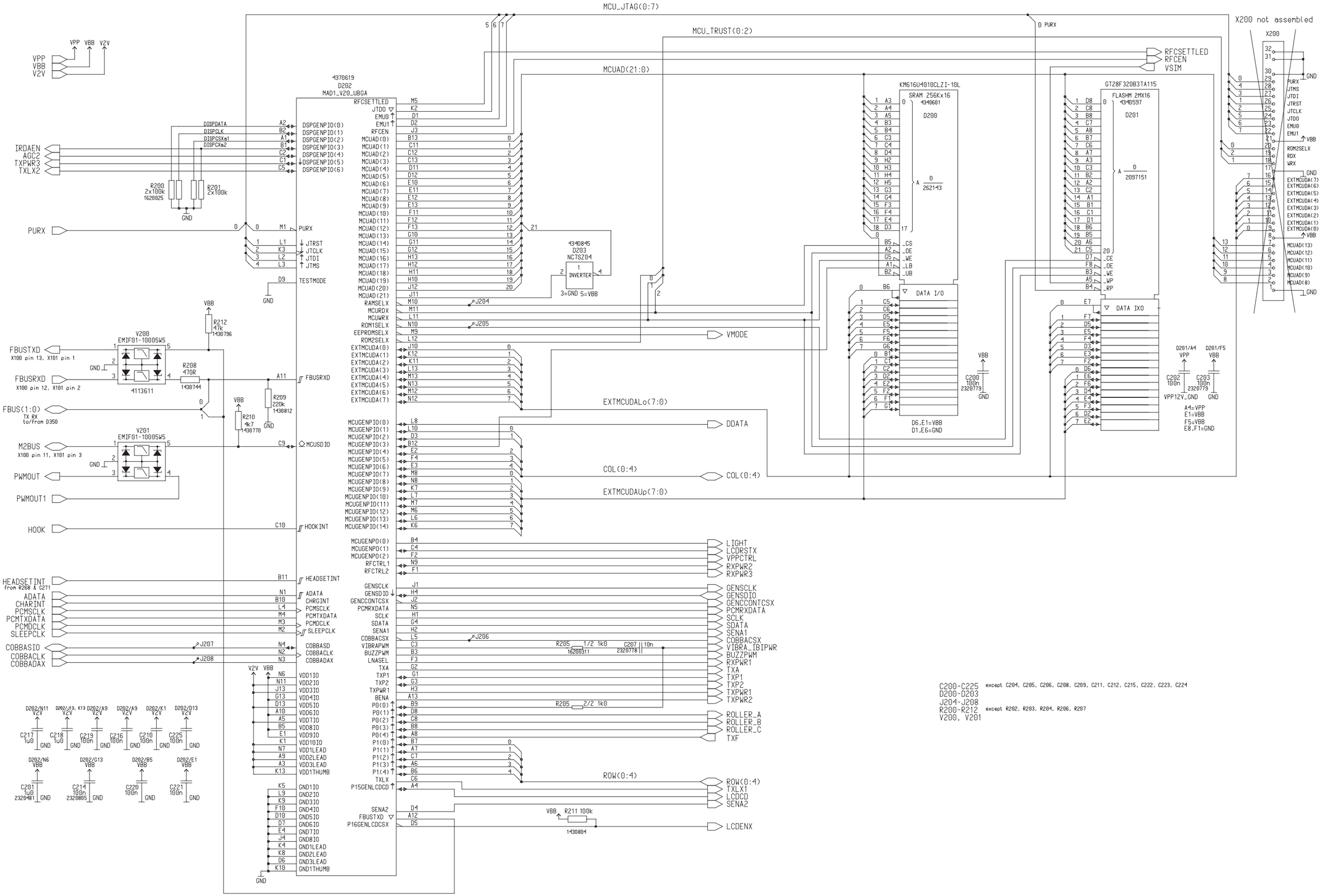
Circuit Diagram of CTRLU Block (Version 17.4 Edit 441)



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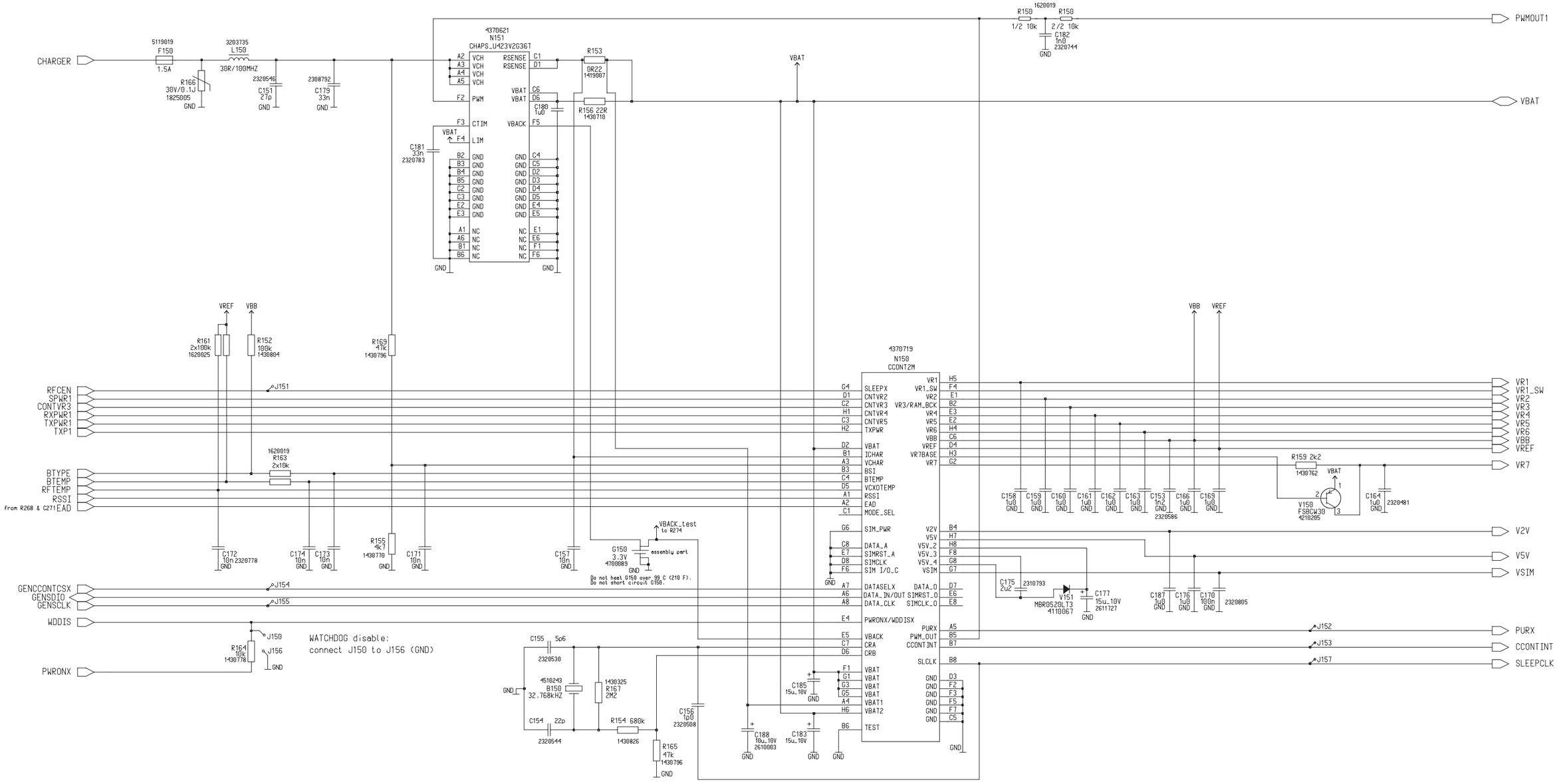
Circuit Diagram of PWRU (Version 17.4 Edit 474)



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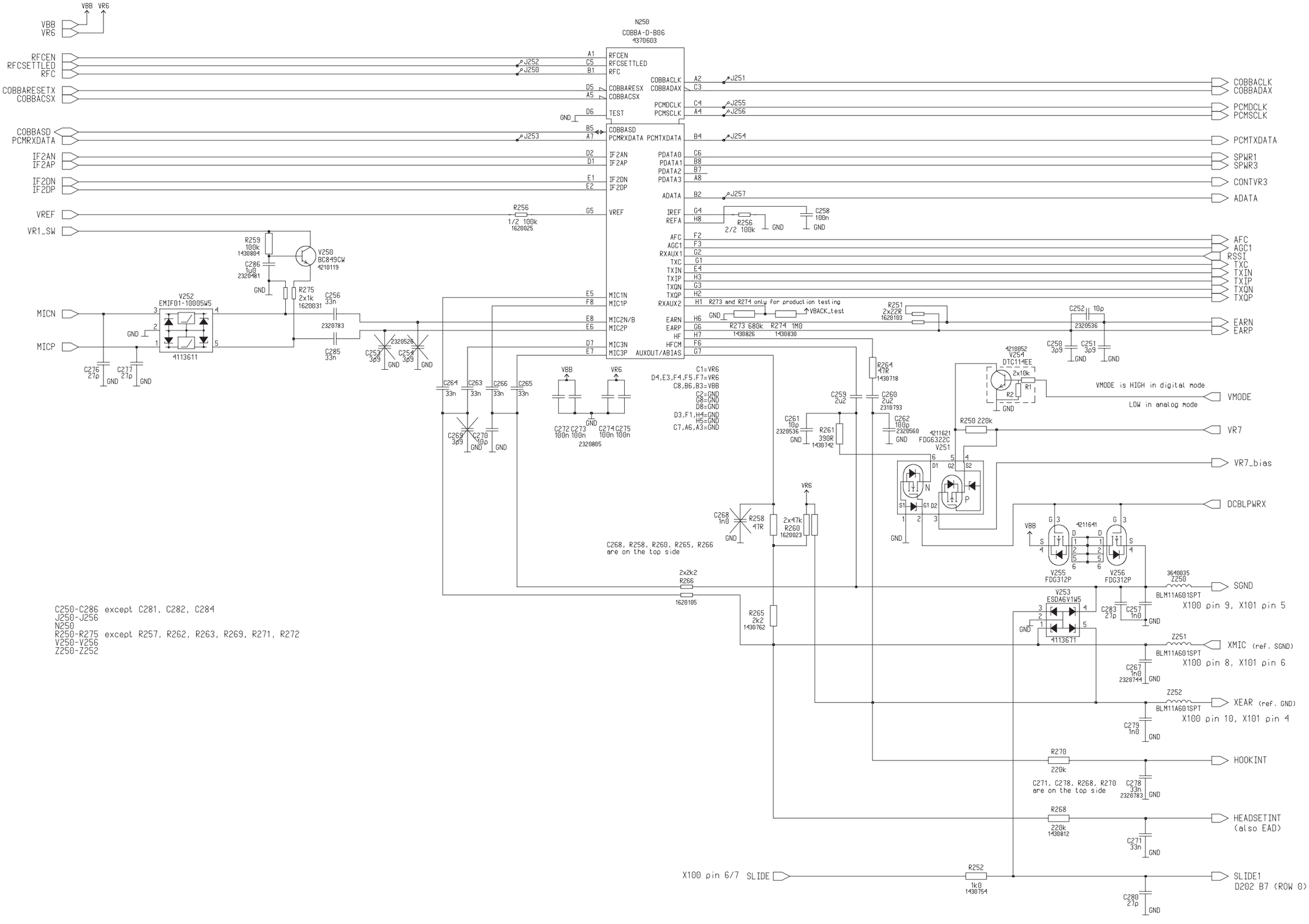
Circuit Diagram of Audio (Version 17.4 Edit 391)



C250-C286 except C281, C282, C284
J250-J256
N250
R250-R275 except R257, R262, R263, R269, R271, R272
V250-V256
Z250-Z252

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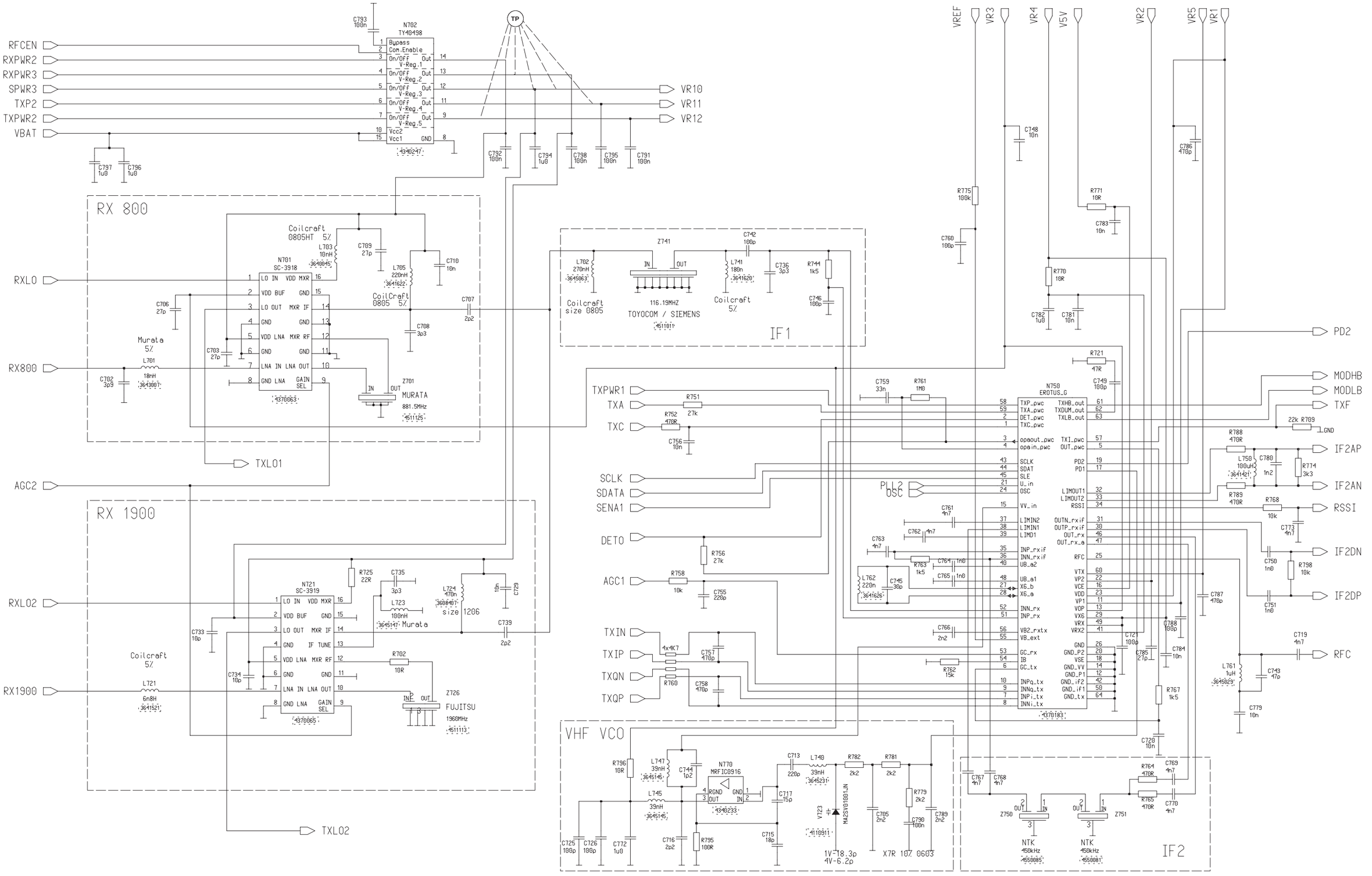
Circuit Diagram of Receiver (Version 17.4.0 Edit 39)



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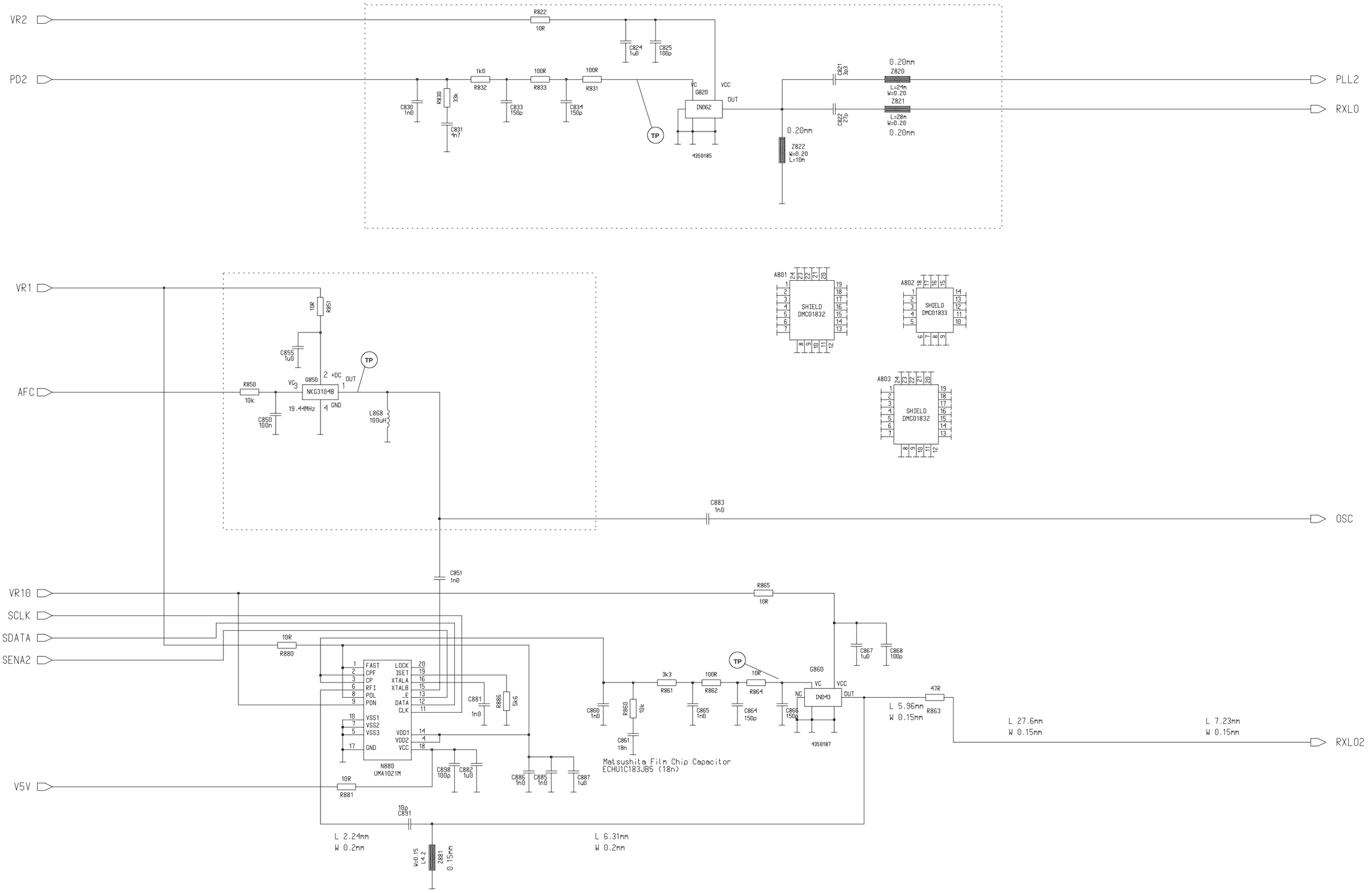
Circuit Diagram of Synthesiser Block (Version 17.4 Edit 17)



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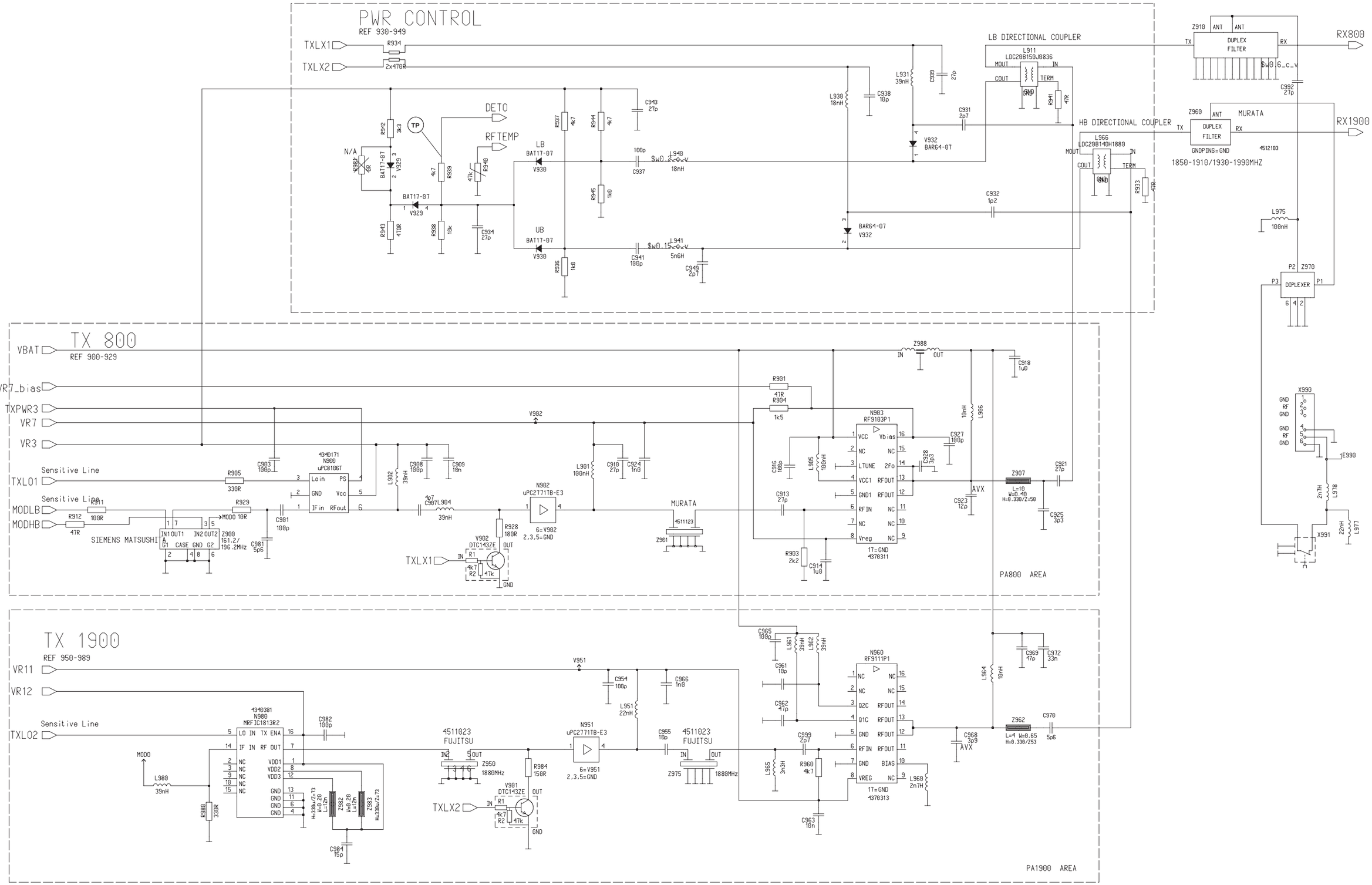
Circuit Diagram of Transmitter (Version 13.0 Edit 51)



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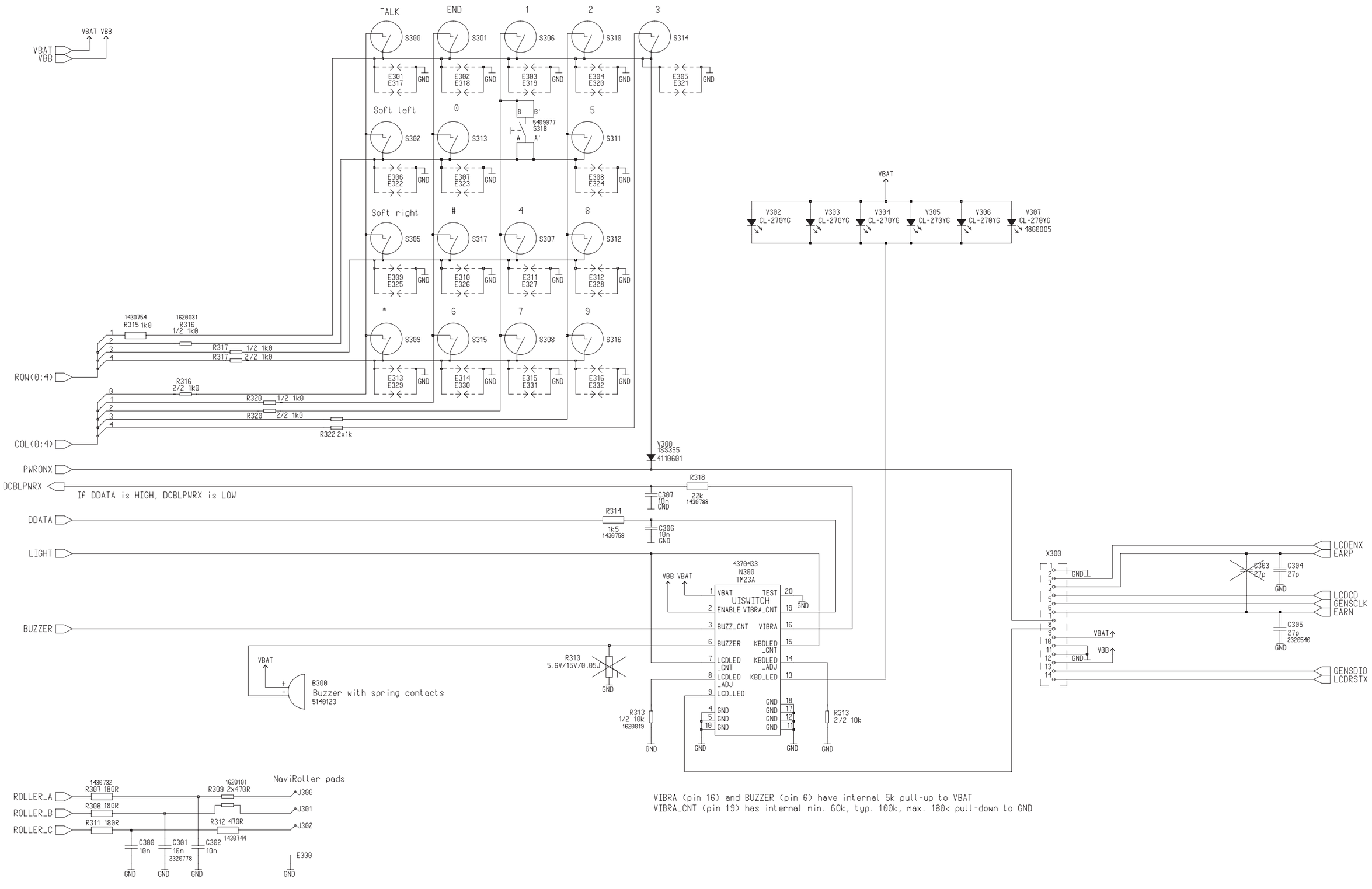
Circuit Diagram of UI (Version 17.4 Edit 179)



VIBRA (pin 16) and BUZZER (pin 6) have internal 5k pull-up to VBAT
VIBRA_CNT (pin 19) has internal min. 60k, typ. 100k, max. 180k pull-down to GND

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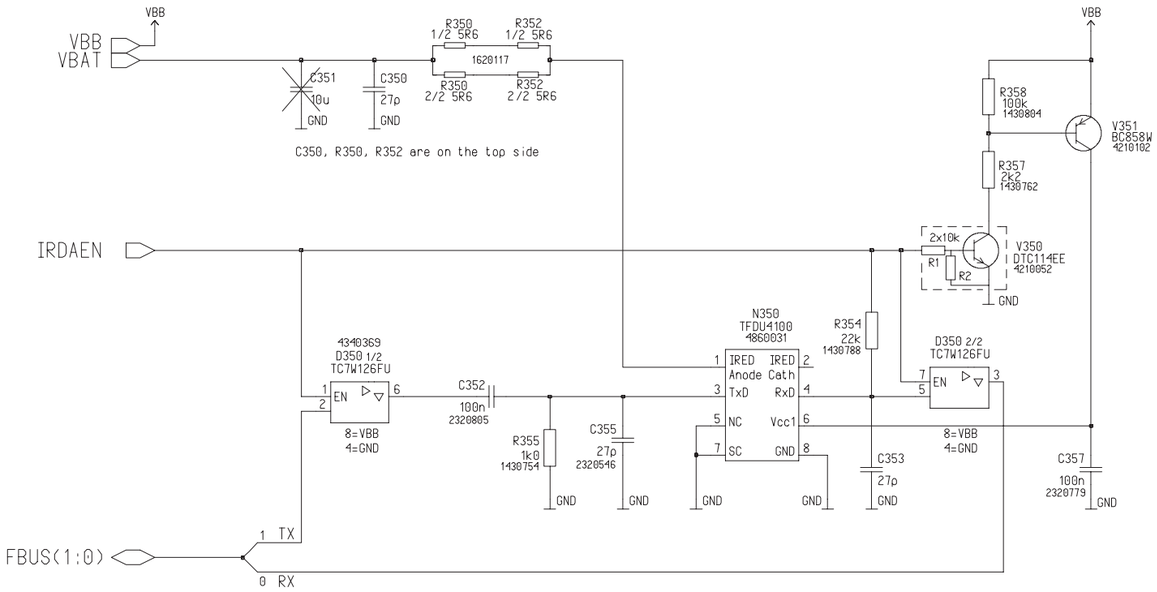
Circuit Diagram of IR (Version 17.4 Edit 99) and BB_RF interface (Version 17.4 Edit 96)



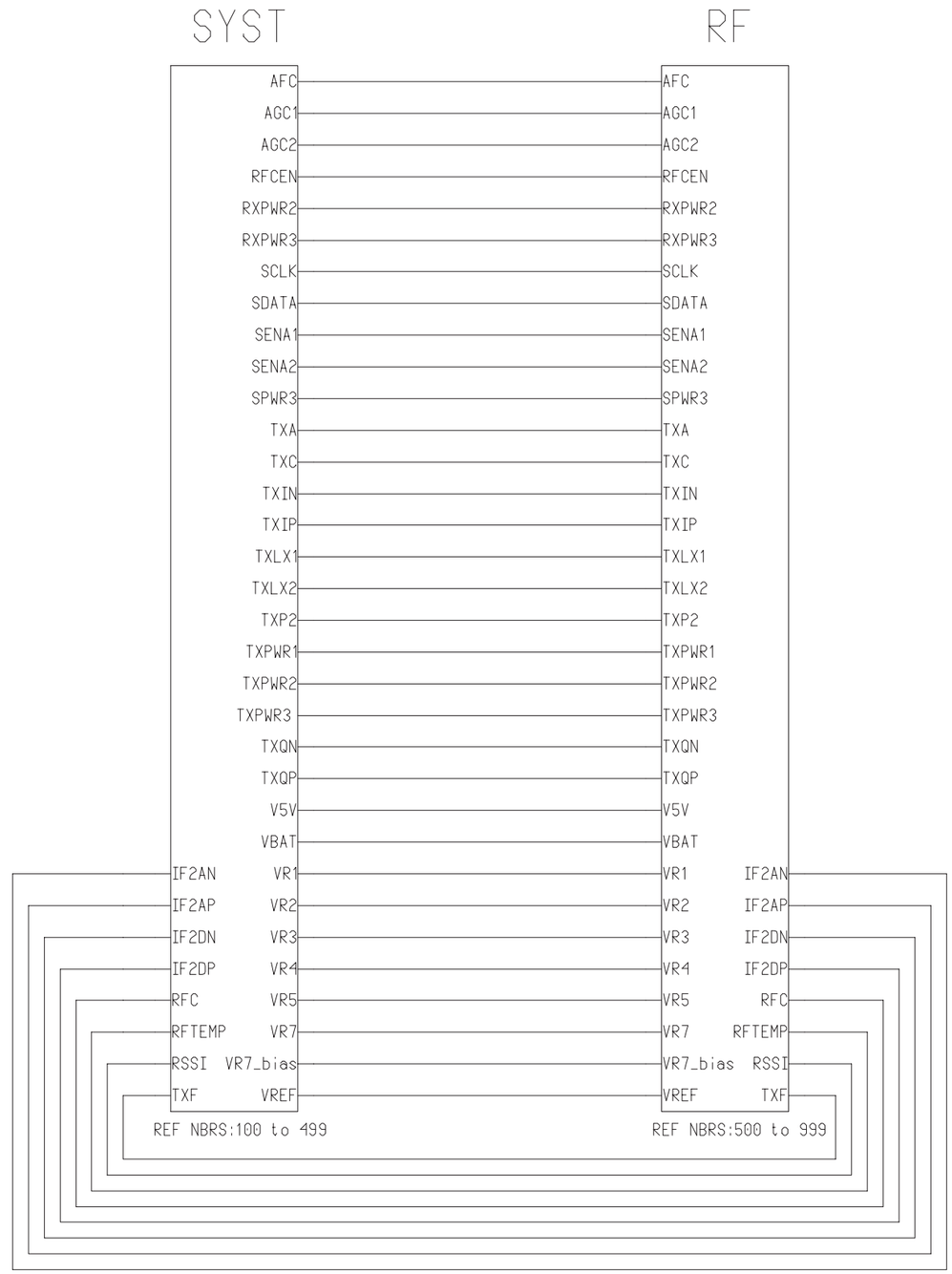


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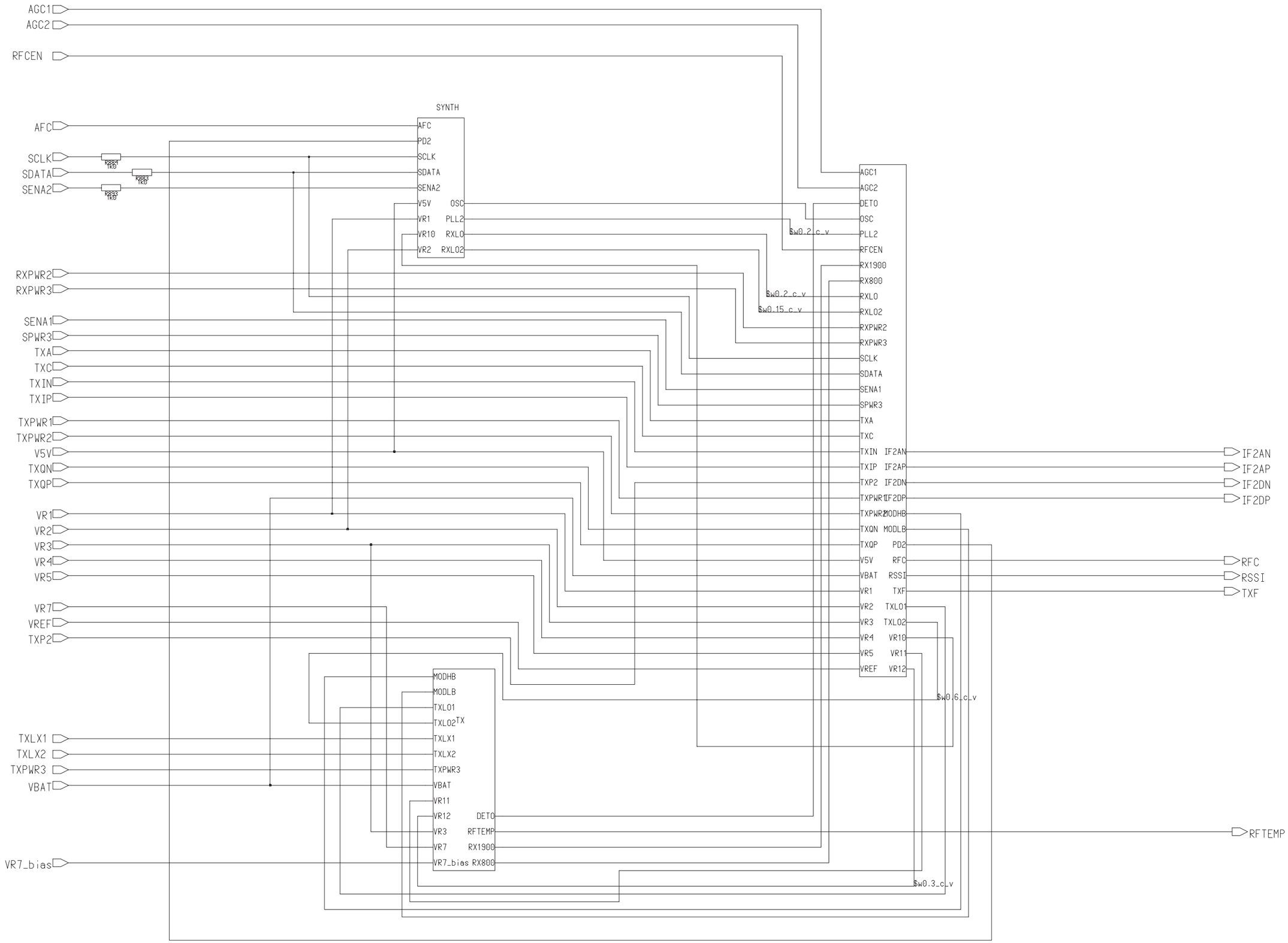
Diagram of Dualband RF (Version 17.4 Edit 10)



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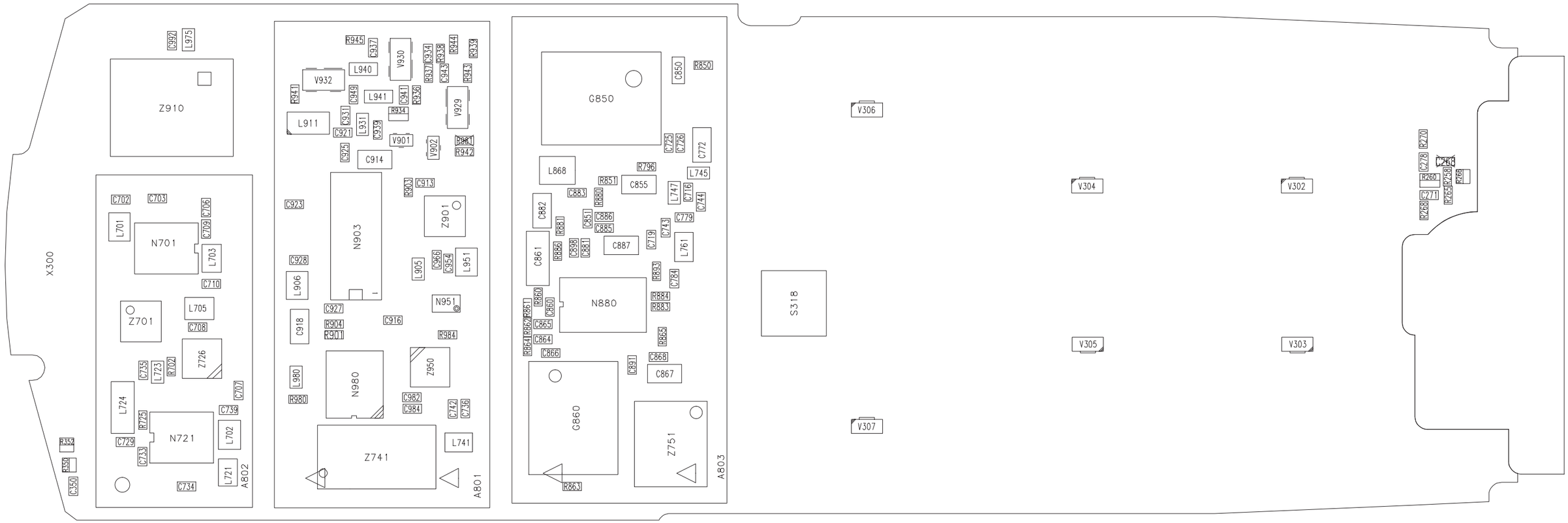
Layout Diagram of UT5U v.17_4 side 1/2



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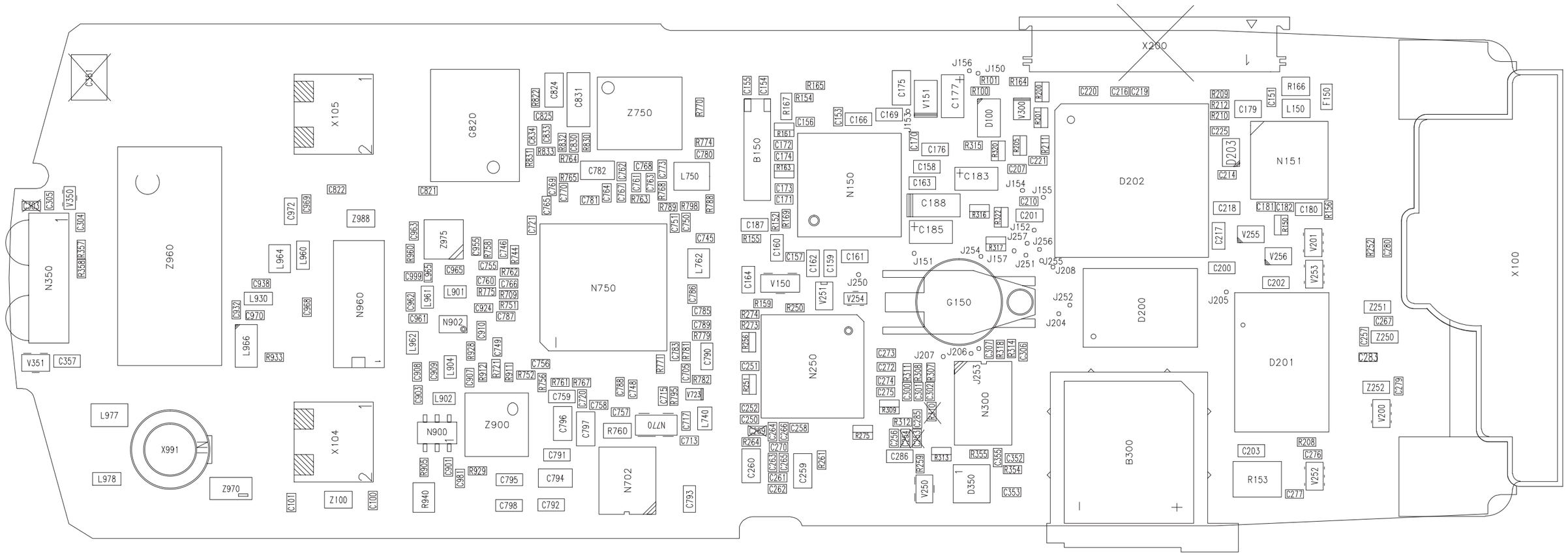
Layout Diagram of UT5U v.17_4 2/2


X200
X105
C820
Z750
C824
C831
C822
C821
Z988
Z960
N350
X104
X991
Z970
Z100
L977
L978
Z975
N902
N900
Z900
N750
L762
L750
L740
N770
N702
L902
L901
L904
L962
L964
L960
L966
L930
N960
C938
C970
C968
C972
C969
C961
C962
C963
C965
C999
C955
C758
C746
C744
C755
C760
C766
C762
C775
C709
C924
C787
C910
C928
C749
C912
C911
C752
C756
C761
C767
C759
C720
C758
C757
C796
C797
R760
C791
C794
C795
C798
C792
C793
C717
C713
C715
C795
V723
C788
C748
C783
C781
C790
C789
C779
C786
C785
C745
C751
C750
C788
C789
C782
C762
C768
C773
C769
C770
C765
C764
C767
C761
C763
C768
C781
R763
R764
R765
R770
R774
R780
R798
R788
R789
R822
R825
R834
R833
R832
R830
R831
R940
R905
R929
C101
C100
C981
C901
C903
C908
C909
C907
C904
C902
C905
C383
V350
C304
R358
R357
C357
V351
R933
C932
C822
C821